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FIG. 1
(Prior Art)

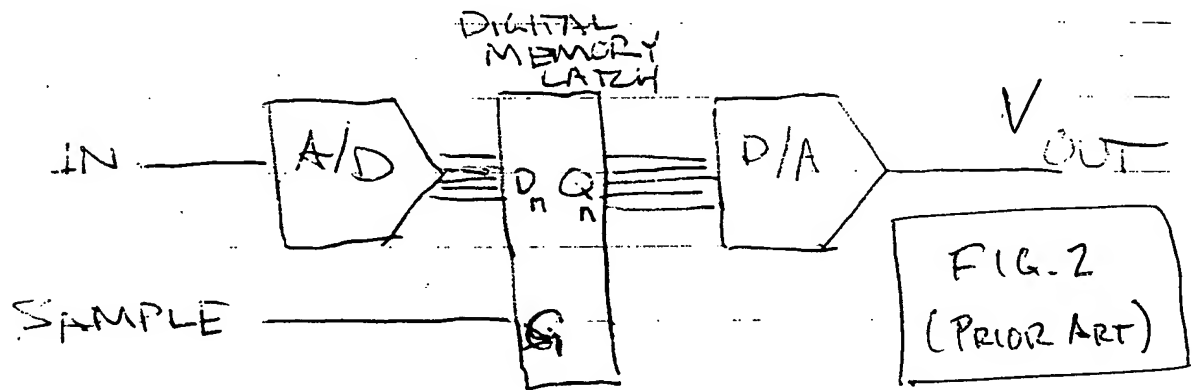
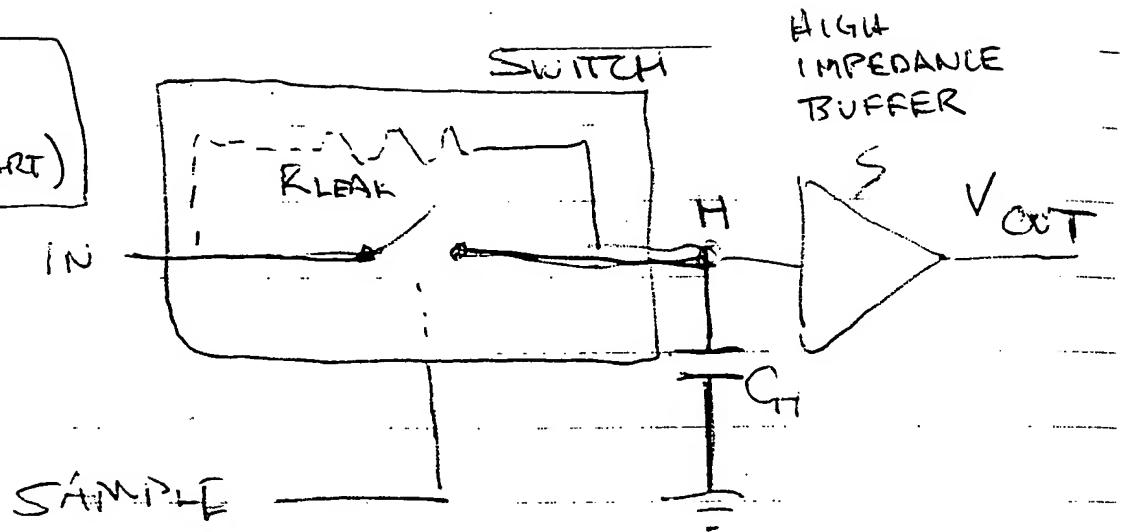
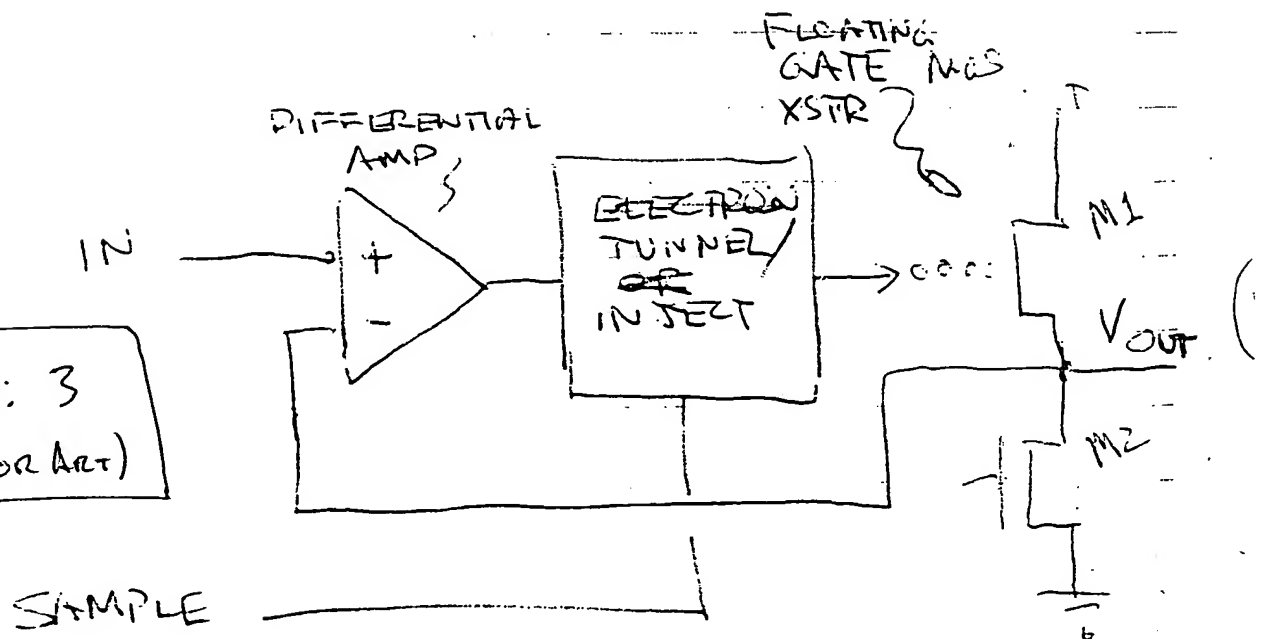


FIG. 3
(Prior Art)



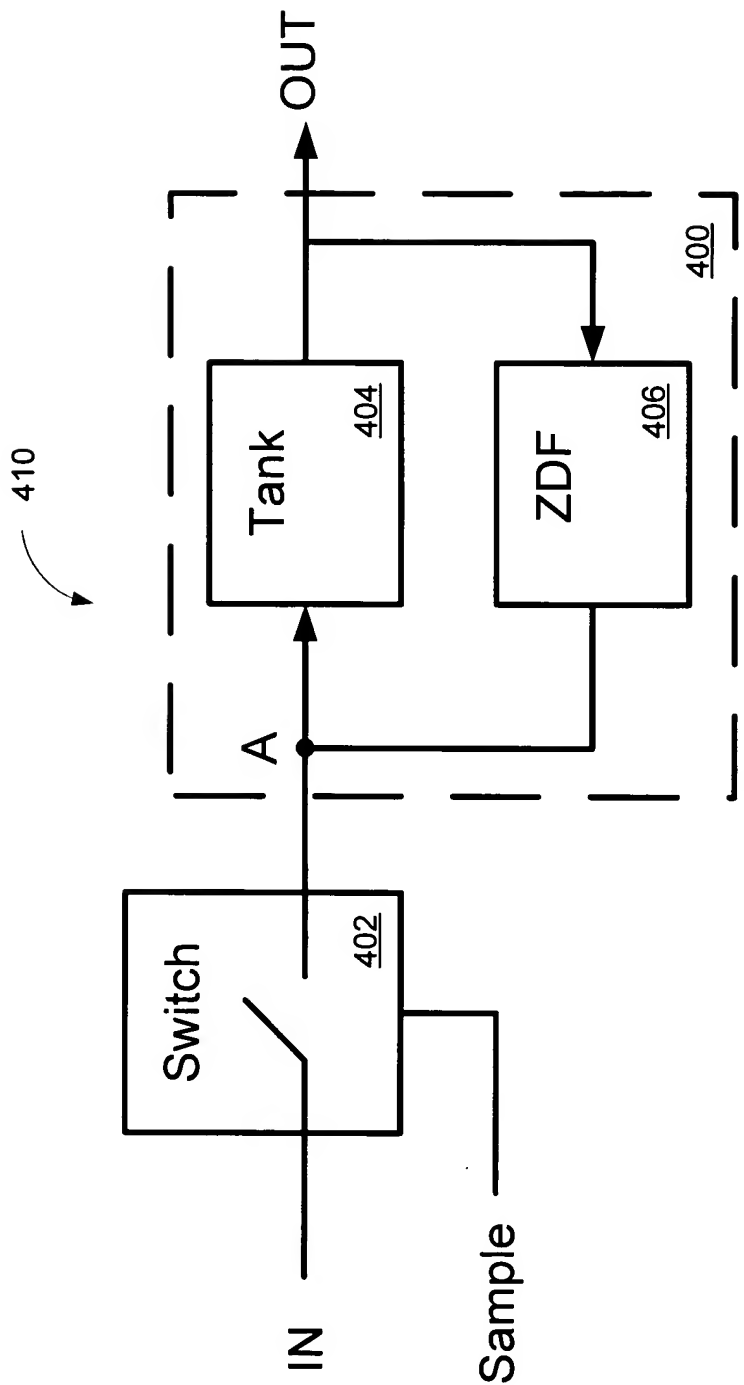
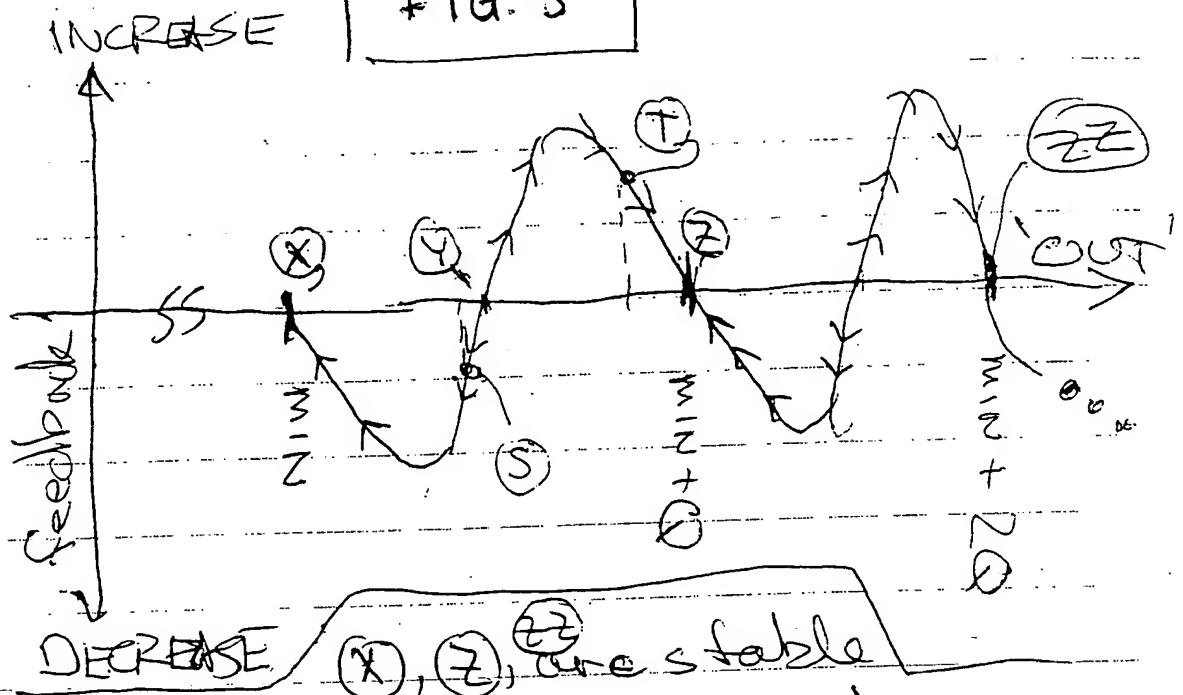


FIG. 4

DETAILS OF 'ZDF' TRANSFER FUNCTION

FIG. 5



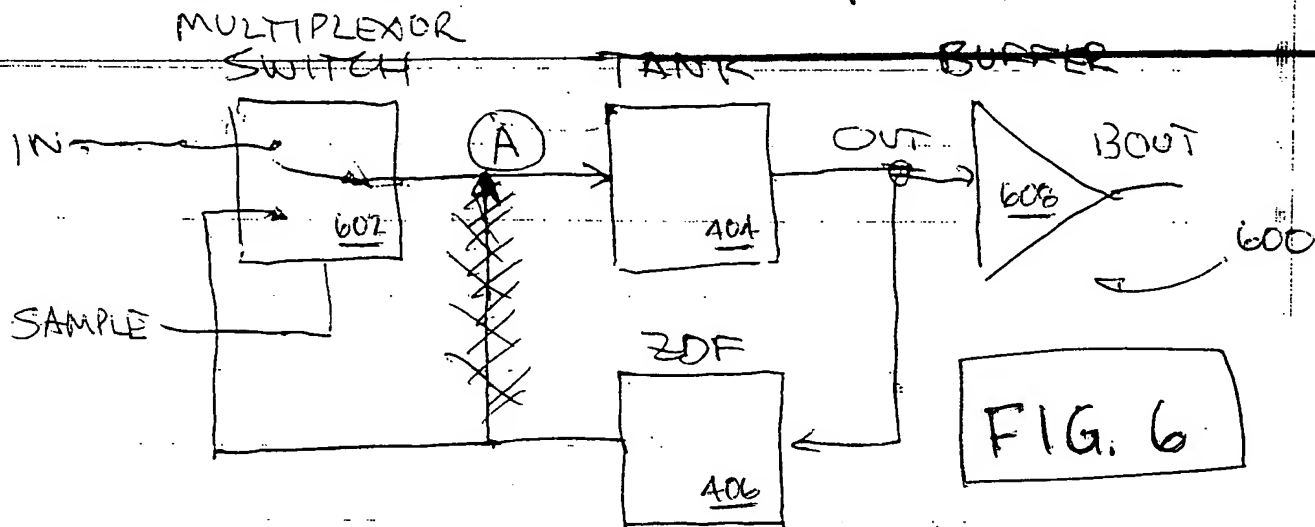
IF OUT IN RANGE $(\text{MIN}, \text{MIN} + Q)$ THEN
FEEDBACK FORCES TO EITHER (X), i.e. 'MIN'
OR (Z) i.e. 'MIN + Q' AS SHOWN BY
ARROWS.

NOTE: PER CONTROL SYSTEM THEORY,
DYNAMICS OF TANK AND ZDF
MUST BE SELECTED TO ACHIEVE
LOOP STABILITY.

SLOPE @ POINT (Y) PREFERABLY STEEP
TO MOVE QUICKLY AWAY FROM (Y)

(7) 2 APRIL 2003 *[Signature]*

ALTERNATIVES: WITH MUX & BUFFER



USE OF MULTIPLEXOR TO ENSURE THAT
 (A) ← 'IN' WHEN 'SAMPLE' AND
 (A) ← 'ZDF FEEDBACK' WHEN NOT 'SAMPLE'

ALTR USE OF BUFFER ON OUT PUT TO
 ENSURE OUTPUT LOAD DOESN'T EFFECT
 TANK 'OUT'.

EMBODIMENT DETAIL OF ZDF

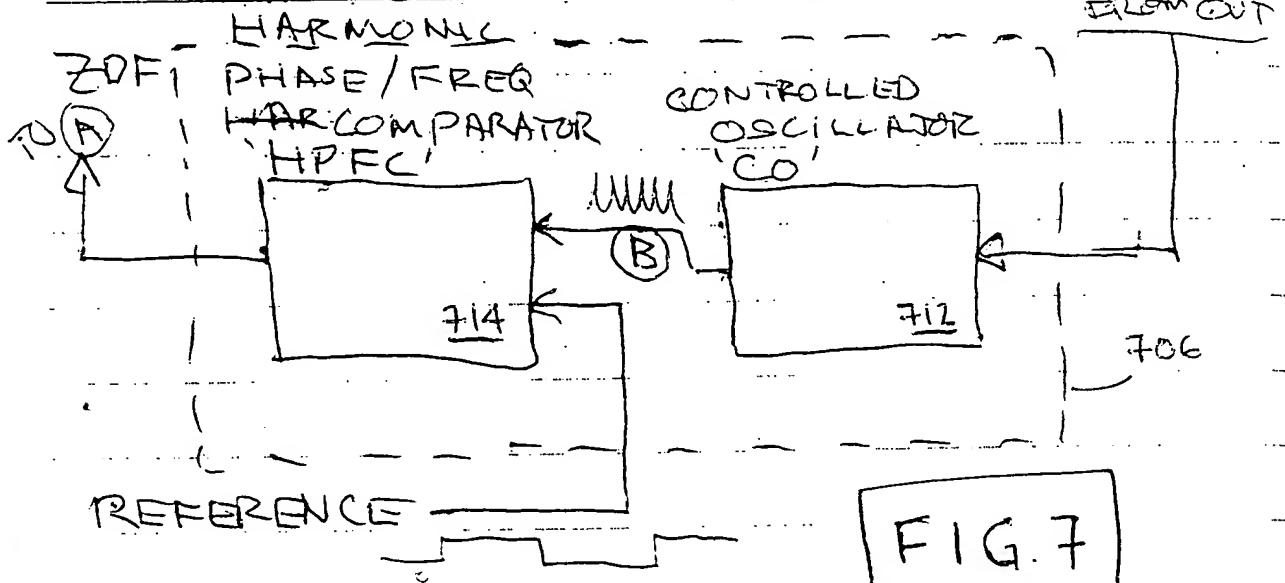
CONTROLLED
 OSCILLATOR

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2 APRIL 2003

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DETAIL OF ZDF IN ONE EMBODIMENT



'CO' AND 'HPFC' WELL KNOWN FROM PHASE LOCK LOOPS. 'HPFC' HAS OUTPUT THAT IS FUNCTION OF COMPARISON BETWEEN FREQUENCY / PHASE OF (B) AND ~~AND~~ MULTIPLES OF FREQUENCY ON REFERENCE. THE QUANTIZATION, Q , IS THE 'CO' FREQUENCY RANGE ~~TOTAL~~ DIVIDED BY THE REFERENCE FREQUENCY.

TYPICAL EG

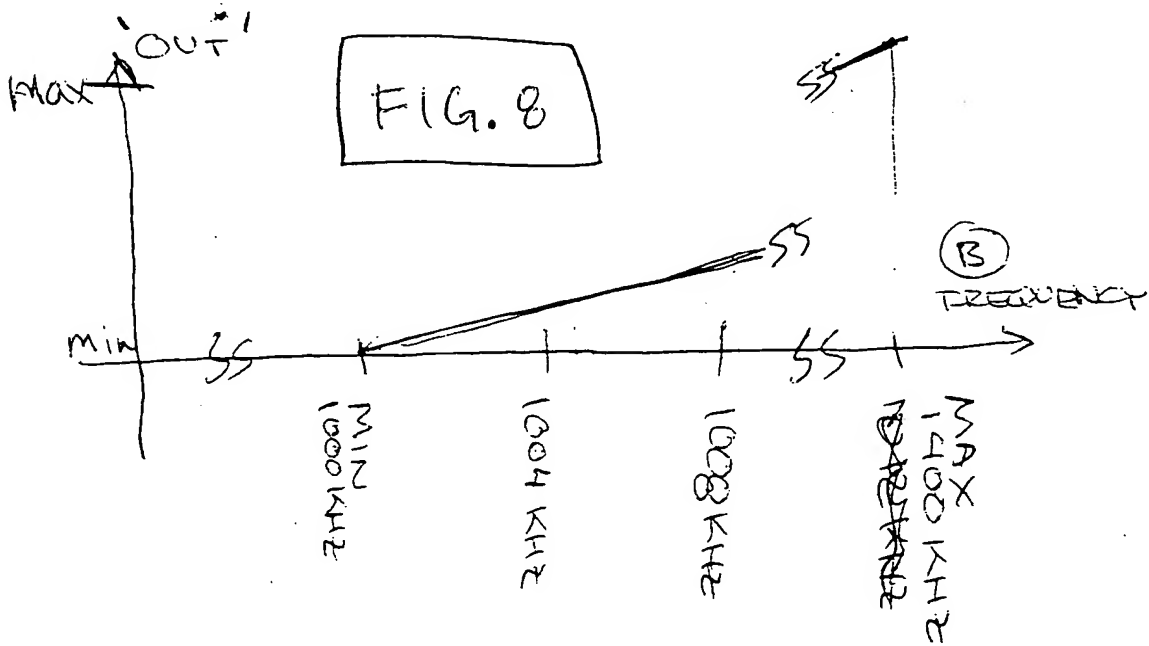
Q \Leftrightarrow 100 : 1 RESOLUTION

'CO' FREQ. MIN = 1000 KHz
MAX = 1400 KHz

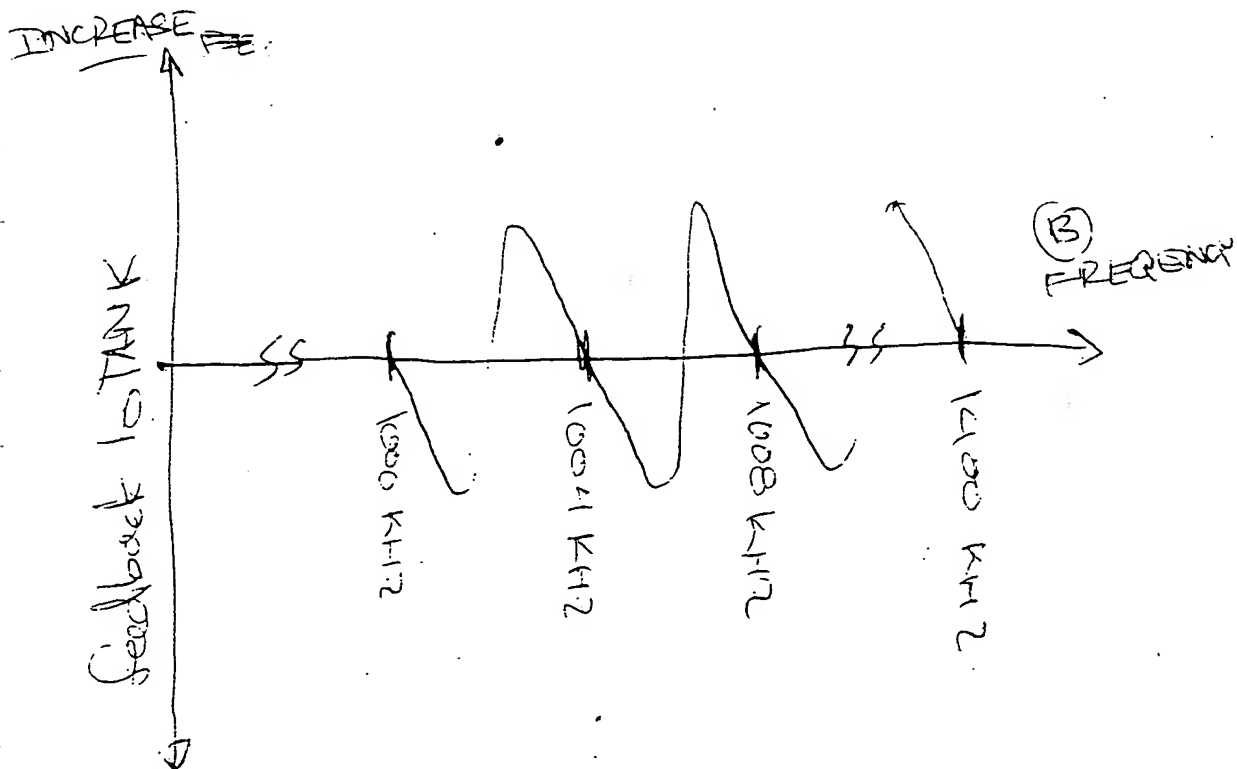
REFERENCE FREQ. = 4KHz

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'CO' FUNCTION



'HPFC' FUNCTION



DECREASE
ZAP 2611003

(10)

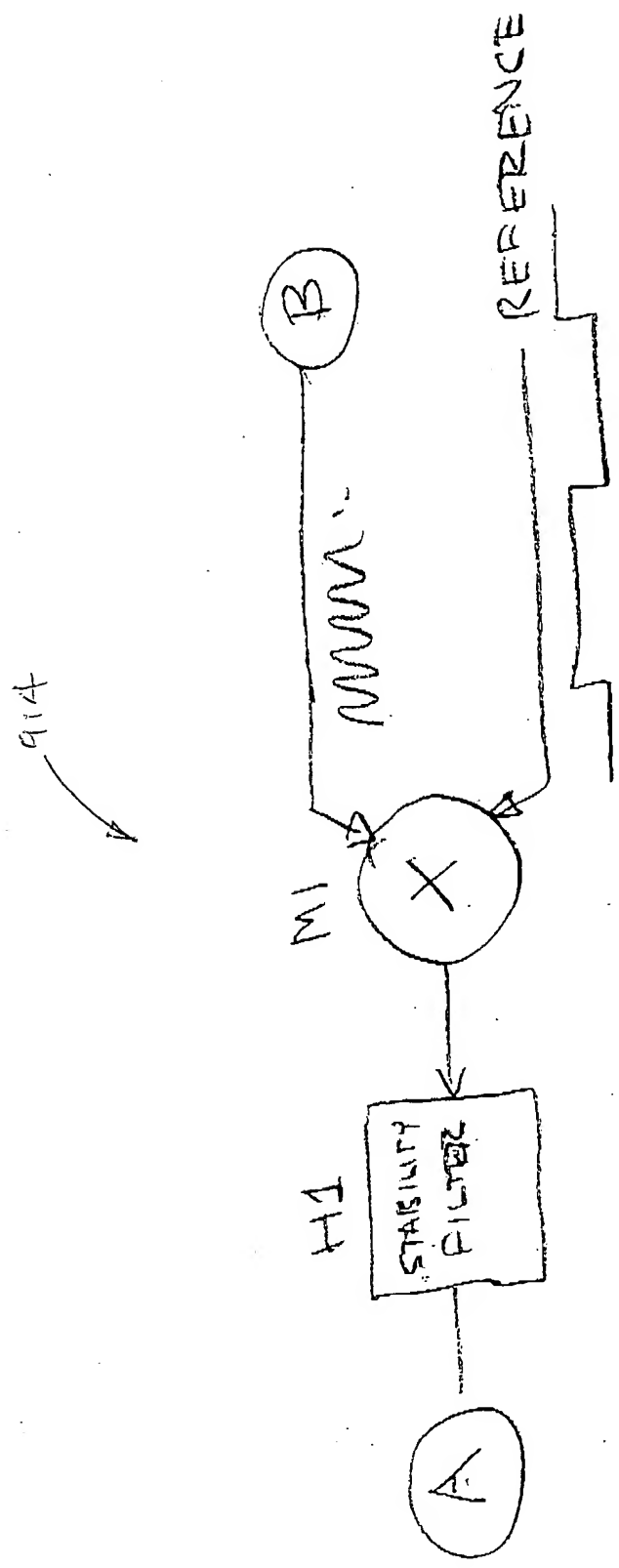


FIG. 9

HPFC

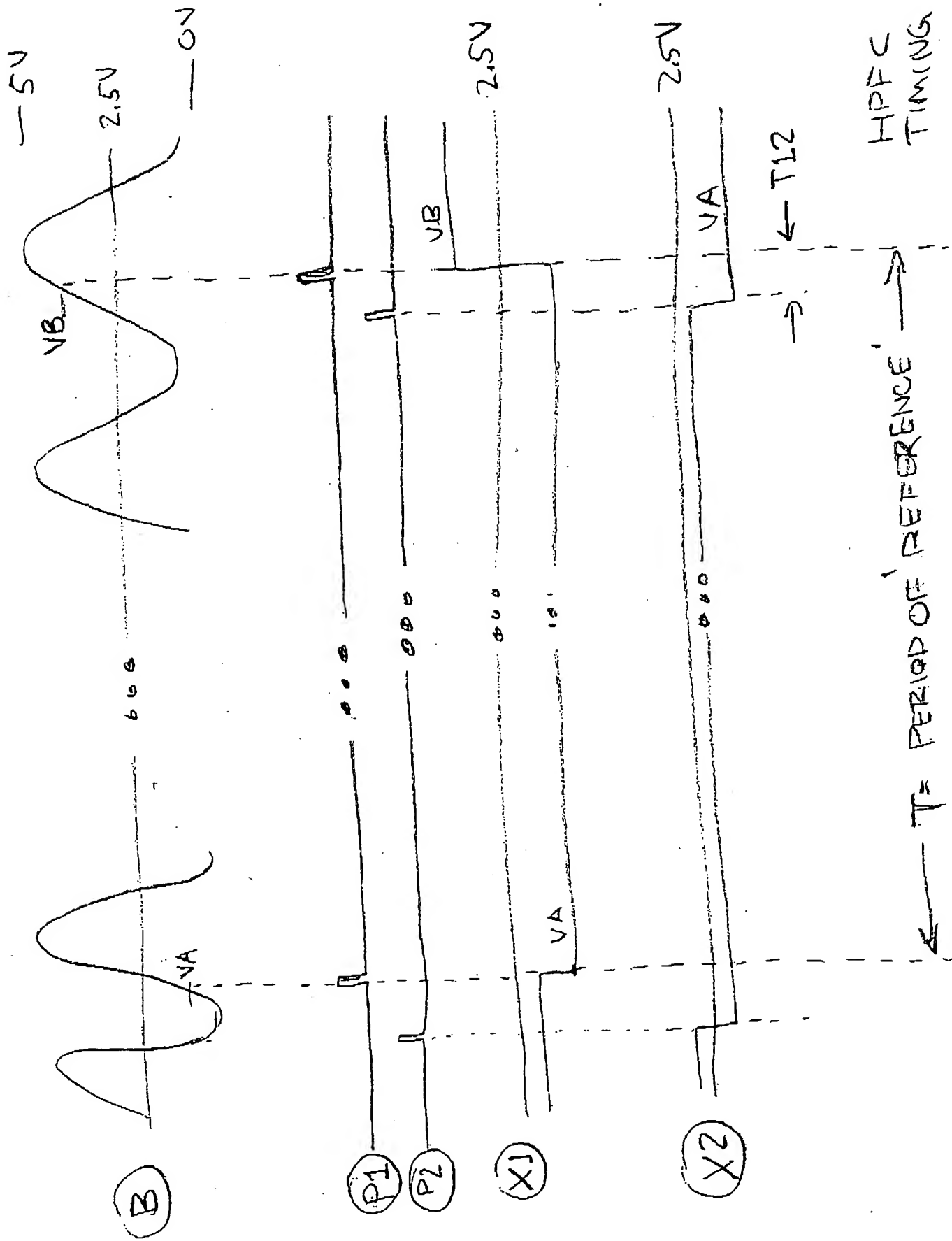


FIG. 11

Switched capacitor sampling of sine VCO at frag of REF, and stabilization via a 1-sample delay added in with coeff of typically 50%

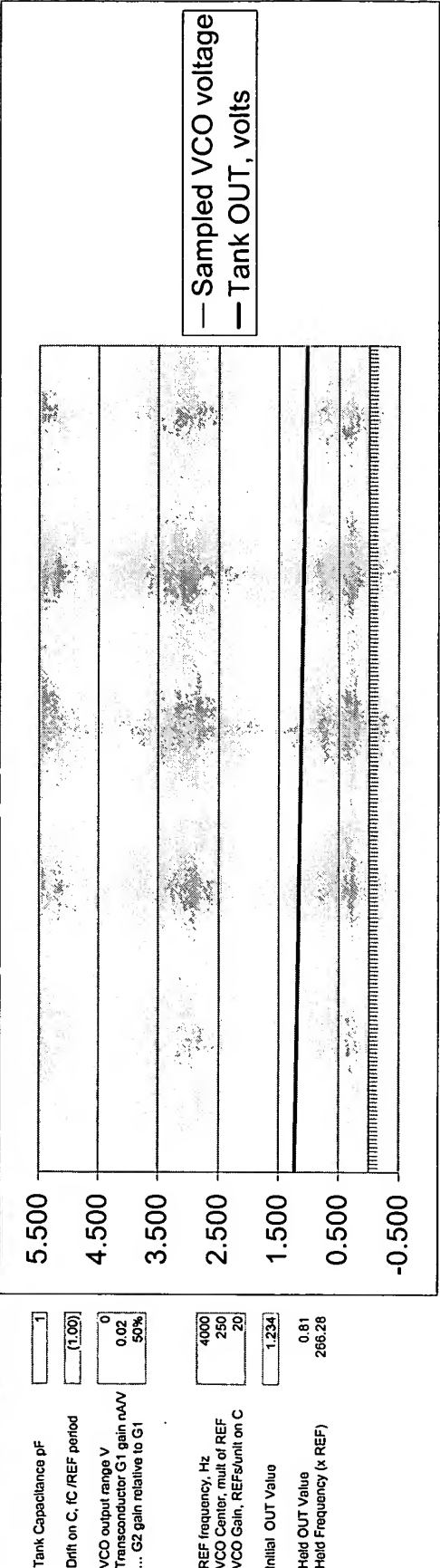


Fig. 12

Switched capacitor sampling of sine VCO at freq of REF, and stabilization via a 1-sample delay added in with coeff of typically 50%

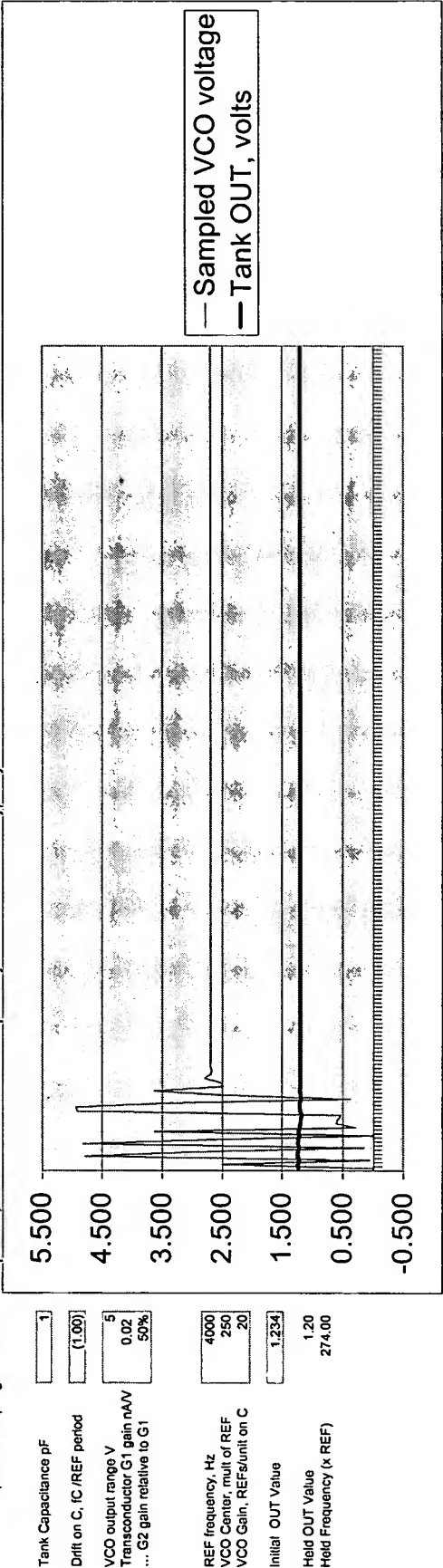


Fig. 13

Switched capacitor sampling of sine VCO at freq of REF, and stabilization via a 1-sample delay added in with coeff of typically 50%

Tank Capacitance pF	1
Drift on C. IC / REF period	(1.00)
VCO output range V	5
Transconductor G1 gain nA/V	0.02
... G2 gain relative to G1	50%
REF frequency, Hz	4000
VCO Center, mult of REF	250
VCO Gain, REF/unit on C	20
Initial OUT Value	1.270
Held OUT Value	1.25
Held Frequency (x REF)	275.00

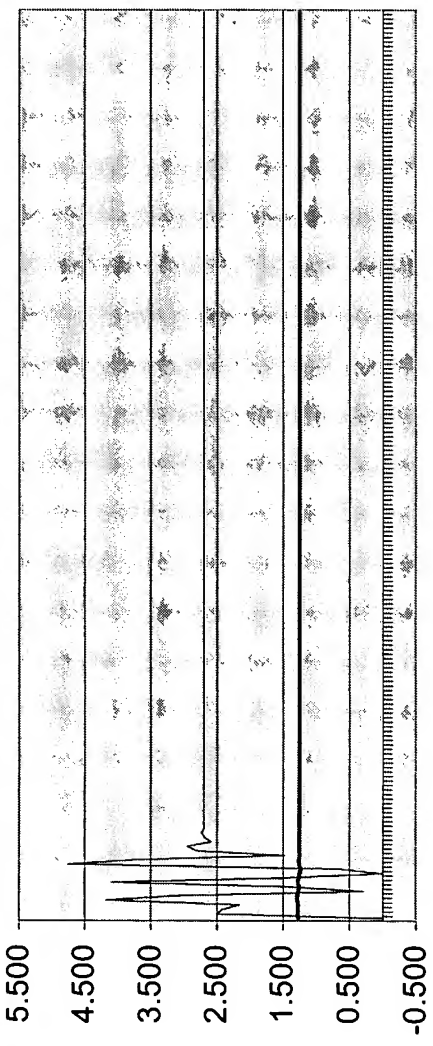


FIG. 14